

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

PATENT

2811

Applicant: John Robertson Tower et al.

: Art Unit: 2811

Serial No.: 09/942,835

: Examiner: S. A. Gebremariam

Filed: August 30, 2001

:

FOR: CCD IMAGER CONSTRUCTED WITH
CMOS FABRICATION TECHNIQUES AND BACK
ILLUMINATED IMAGER WITH IMPROVED
LIGHT CAPTURE

: Confirmation No.: 9999

7/Election
T. Steptoe
10-25-02

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TECHNOLOGY CENTER 2800

RESPONSE TO RESTRICTION REQUIREMENT

Assistant Commissioner for Patents

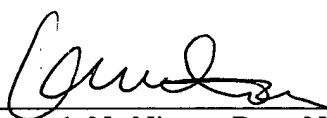
Washington, D.C. 20231

S I R :

This is in response to the Restriction Requirement stated in the Office Letter dated October 1, 2002.

The examiner requires that claims of either Group I or Group II be elected for prosecution. Applicant elects to prosecute Group I (claims 1 to 21, drawn to a semiconductor integrated circuit device, classified in class 257, subclass 228). This election is made without traverse.

Respectfully submitted,


Kenneth N. Nigon, Reg. No. 31,549
Attorney for Applicant

Dated: October 14, 2002

Suite 301
One Westlakes, Berwyn
P.O. Box 980
Valley Forge, PA 19482-0980
(610) 407-0700

The Assistant Commissioner for Patents is hereby authorized to charge payment to Deposit Account No. 18-0350 of any fees associated with this communication.

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to: Assistant Commissioner for Patents, Washington, D.C. 20231 on:

October 14, 2002


Kenneth N. Nigon